

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	66	(fukui near2 kenichi).in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/28 20:47
L2	105	(hiraki near2 mitsuru).in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/28 20:48
L3	208	(ito near2 takayasu).in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/28 20:48
L4	360	1 2 3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/28 20:50
L5	323818	((integrated adj1 circuit) IC).ab.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/28 20:50
L6	64	4 and 5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/28 20:53
L7	8889	first near3 reference near3 (generator generating generation generated)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/28 20:57
L8	6702	internal adj1 voltage	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/28 20:57

L9	1325	(trim trimming trimmed) adj3 (switch switching switched transistor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/28 20:58
L10	1	7 same 8 same 9	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/28 20:59